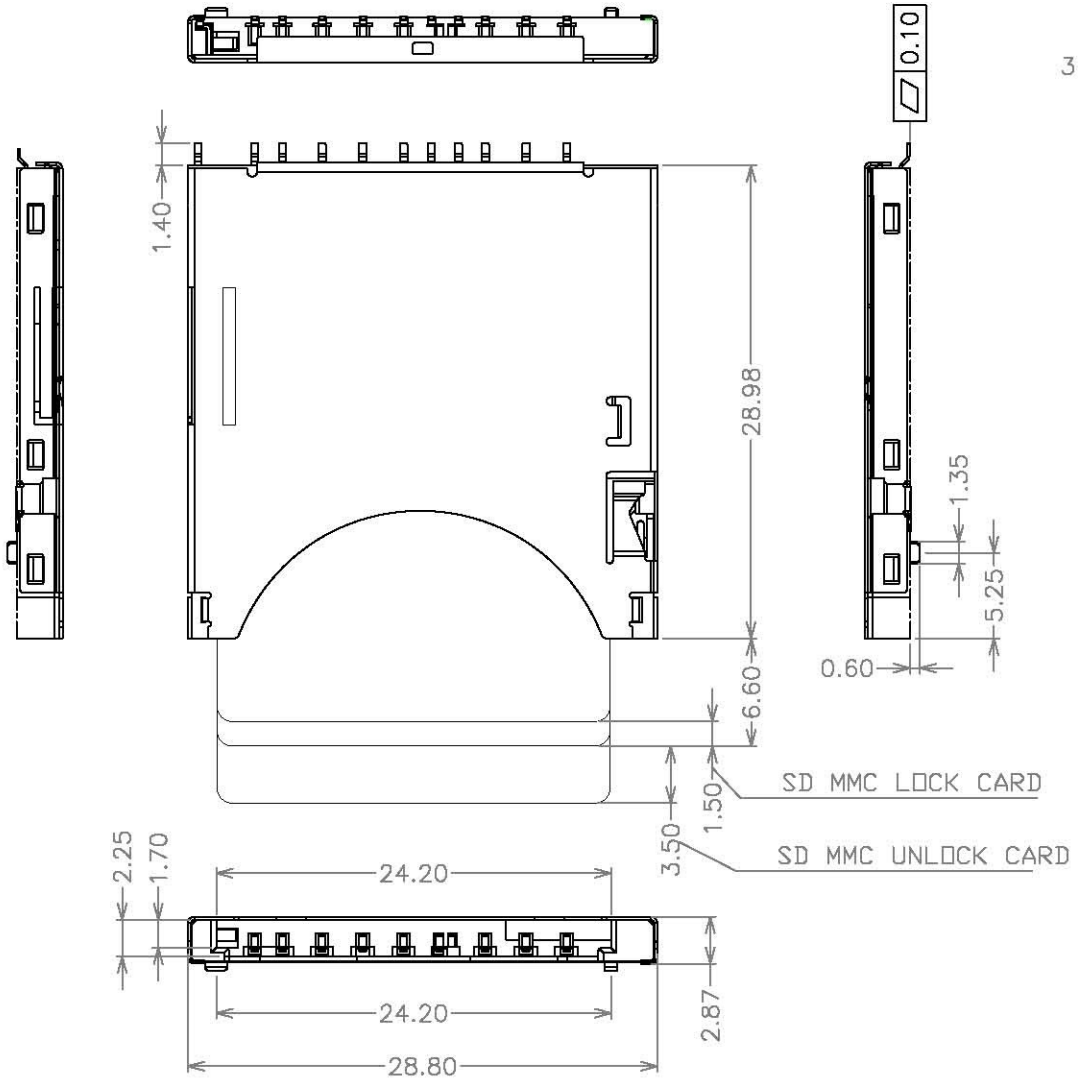
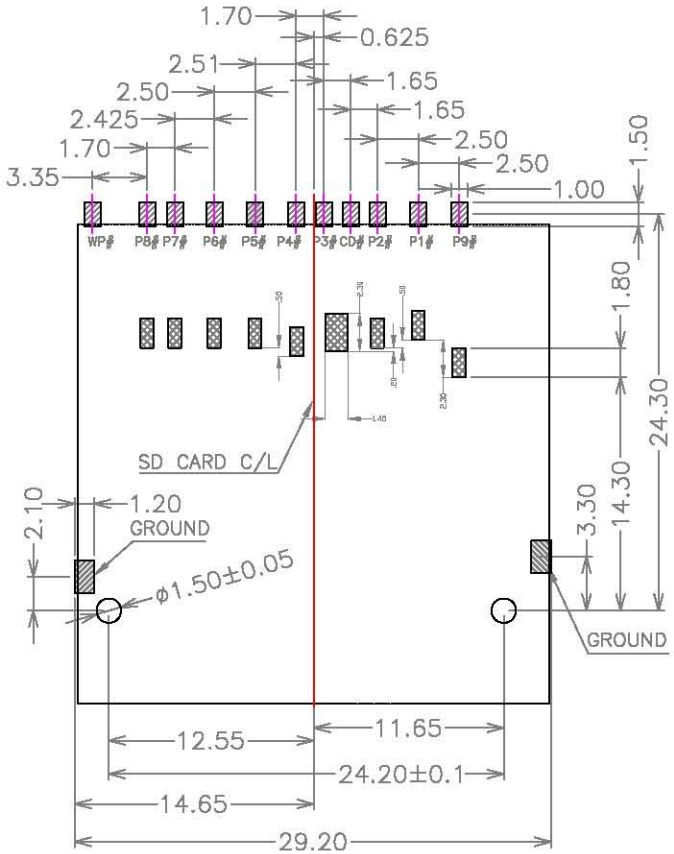
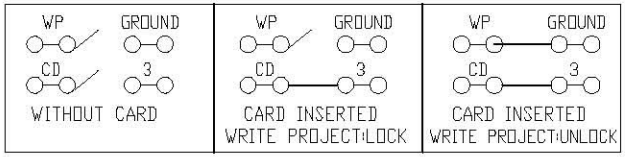




Pad Area
Keep Out Area

PCB LAYOUT TOP VIEW
(TOLERANCE:±0.05)

SD CARD PIN DESIGN			
Pin#	Name	Type	Card Detect/ Data Line[Bit3]
1	CD/DAT3	I/O/PP	Command/Response
2	CMD	PP	Supply voltage
3	Vss1	S	Supply voltage ground
4	VDD	S	Supply voltage
5	CLK	I	Clock
6	Vss2	S	Supply voltage ground
7	DAT0	I/O/PP	Date Line [Bit0]
8	DAT1	I/O/PP	Date Line [Bit1]
9	DAT2	I/O/PP	Date Line [Bit2]



L&X 深圳市连欣科技有限公司

TOLERANCE: X.X ±0.30, X.XX ±0.25, X.XXX ±0.15, X.° ±2°, X.X° ±0.5°	DRAWED BY : 陈一鸣	DATE : 2014-02-23	PART NAME: SD PUSH 带锁卡	
	CHECKED BY: 马跃	DATE : 2014-02-23	PART NO.	XFSD-0527-010
UNIT: mm [inch]	APPROVED BY: 邱国夷	DATE : 2014-02-23	MOLD NO.	
SCALE:1:1 SIZE: A4			DRAW NO:	SIM102010
			SHEET NO.	1 OF 1