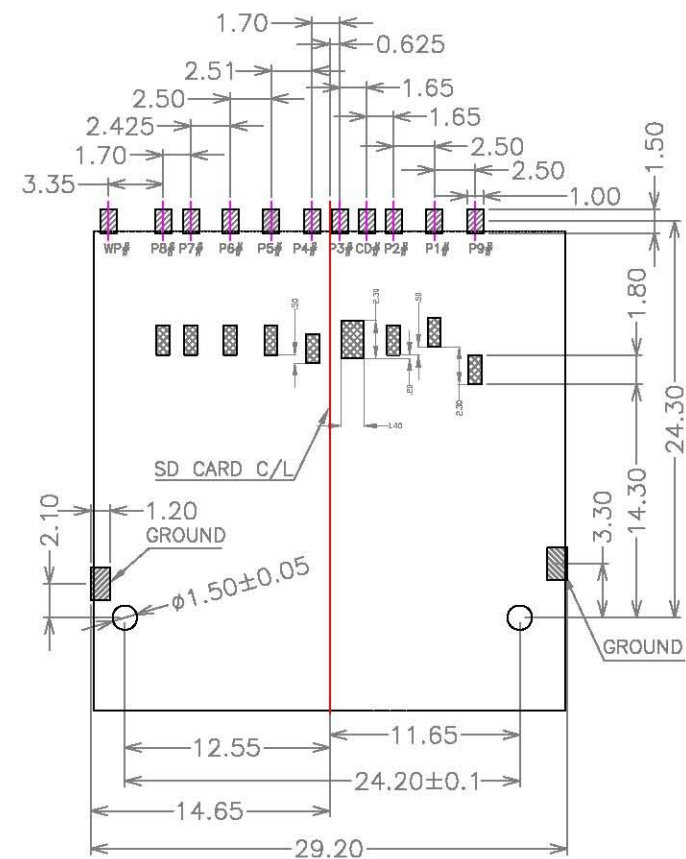




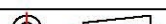
SD CARD PIN DESIGN			
Pin#	Name	Type	Card Detect/ Data Line(s)
1	CD/DAT3	I/O/PP	Command/Response
2	CMD	PP	Supply voltage
3	Vss1	S	Supply voltage ground
4	VDD	S	Supply voltage
5	CLK	I	Clock
6	Vss2	S	Supply voltage ground
7	DAT0	I/O/PP	Date Line [Bit0]
8	DAT1	I/O/PP	Date Line [Bit1]
9	DAT2	I/O/PP	Date Line [Bit2]

 Pad Area
 Keep Out Area
 PCB LAYOUT TOP VIEW
 (TOLERANCE:±0.05)

Figure 1 consists of three circuit diagrams illustrating the effect of card insertion on the WP (Write Protect) and CD (Card Detect) lines. Each diagram shows two nodes, WP and CD, connected to ground through a switch labeled '3'.

- WITHOUT CARD:** The WP and CD lines are shown as open circuits, indicating no connection to ground.
- CARD INSERTED WRITE PROJECT-LOCK:** The WP and CD lines are connected to ground through a switch labeled '3', indicating a locked state.
- CARD INSERTED WRITE PROJECT-UNLOCK:** The WP and CD lines are connected to ground through a switch labeled '3', indicating an unlocked state.

L&X 深圳市连欣科技有限公司

TOLERANCE:		DRAWN BY :	DATE :	PART NAME:	
X.X ±0.30		陈一鸣	2014-02-23	SD PUSH 带锁卡	
X.XX ±0.25		CHECKED BY:	DATE :	PART NO.	XFSD-0527-010
X.XXX ±0.15				MOLD NO.	
X.* ±2* X.X ±0.5*		马跃	2014-02-23	DRAW NO:	STM102010
		APPROVED BY:	DATE :	SHEET NO.	1 OF 1
UNIT: mm	[inch]	邱国夷	2014-02-23		
SCALE:1:1	SIZE: A4				